# **Benha University Benha Faculty of Engineering** Time: 3 Hours.

**30 December,** 2017 **Final Exam** 

Examinar: Dr. Tamer Omar

**Department: Electrical engineering** 

3<sup>rd</sup>Year Course Exam

**Subject**: Microprocessor Based Systems (a)



#### AQ1:

MOV SI,2200H

MOV DI, 2300H

MOV CX,32H

MOV AL, [SI] BACK:

> AND AL, 01h JNZ SKIP

MOV [DI], AL

INC DI

SKIP: INC SI

LOOP BACK

# AQ2

mov sum, 0 mov cx, 1

whileSum: cmp sum, 1000 ;sum < 1000?

> jnl endWhileSum ; exit if not cx, 50 ; count <= 24 cmp jnle endWhileSum ; exit if not

; body of loop add sum, cx

inc cx

whileSum ; go check condition again jmp

endWhileSum:

## AQ3

Solution. The control word is decided as given as follows:

В7 B6 B5 В4 ВЗ В2 В1 B0 Control word 1 0 0 0 0 1 0 = 82 H Port A in Port A, Port C, Port B, Port B, Port C, mode mode 0 O/P O/P mode 0



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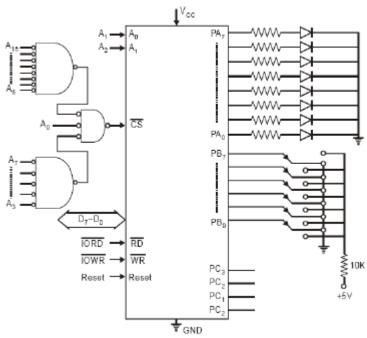
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The interface circuit is as shown below:



82H is the control word. The control word format for BSR mode is as shown ahead :

8255	I/O Address lines												Hex port				
ports	A <sub>15</sub>	$A_{14}$	A <sub>13</sub>	$A_{12}$	A <sub>11</sub>	$A_{10}$	$A_9$	A <sub>8</sub>	$A_7$	A <sub>β</sub>	$A_5$	$A_4$	$A_3$	$A_2$	$A_1$	$A_0$	address
Port A	0	0	0	0	0	1	1	1	0	1	0	0	0	0	0	0	0740H
Port B	0	0	0	0	0	1	1	1	0	1	0	0	0	0	1	0	0742H
Port C	0	0	0	0	0	1	1	1	0	1	0	0	0	1	0	0	0744H
CWR	0	0	0	0	0	1	1	1	0	1	0	0	0	1	1	0	D746H

The ALP for the problem is shown below:

	MOV DX, 0746H;	Initialise CWR with
	MOV AL, 82H;	Control word 82H
	OUT DX, AL;	
	SUB DX, 04;	Get address of port B in DX
	IN AL, DX;	Read port B for switch
	84B DX, 02;	Positions into AL and get port A address
	MOV BL, 00H;	Initialise BL for switch count
	MOV CH, 08H;	Initialise CH for total switch number
Label 1 :	ROL AL;	Rotate AL through carry to check
	JNC label 2	
	INC BL	
Label 2 :	DEC CH	
	JNZ label 1	
	MOV AL, BL	
	ADD DX, OH	
	OUT DX, AL	
	HLT	



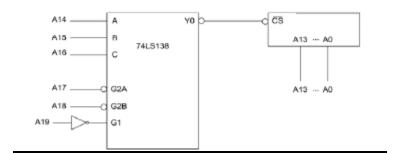
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*Q4* 



```
Y0:
       00000:03FFF
Y1:
       04000:07FFF
Y2:
       08000:0BFFF
Y3:
       OCOOO:OFFFF
Y4:
       10000:13FFF
Y5:
       14000:17FFF
Y6:
       18000:1BFFF
       1C000:1FFFF
Y7:
Q5
              MOV SI,0300H
              MOV BL, 10
              CALL SUMP
              MOV BH, AL
              MOV SI,0050H
              MOV BL, 100
              CALL SUMP
              MOV AH, 0
              DIV BH
              MOV [0400H], AL
              MOV [0401H], AH
              INT 20H
SUMP:
              MOV DI, SI
              MOV CL, BL
              MOV AL, 0
ALPHA: ADD AL, [DI]
              INC SI
              DEC CL
              JNZ ALPHA
```

RET

Good Luck